N THE UNITED STATES PATENT AND TRADEMARK OFFICE

2491

**APPLICATION** 

Adolf Bernds et al.

OF:

**SERIAL NO:** 

10/542,678

Group Art Unit:

2891

FILED:

December 27, 2005

Examiner

Not assigned

ATTY/DKT NO.:

411000-140

Customer No.

27162

FOR:

Organic Electronic Component and Method for Producing Organic

**Electronic Devices** 

Mail Stop Amendment Commissioner for Patents P.O. Box 1450 Alexandria, VA 22313-1450

## **DISCLOSURE STATEMENT UNDER 37 CFR 1.56**

SIR:

This paper is to bring to the attention of the PTO the following U.S. copending applications, all of which are related in different respects to organic electronic devices and/or method of making such devices such as transistors, diodes, integrated circuits and the like and/or related technologies. Many of these applications and the instant above-entitled application may have one or more common inventors.

This paper is submitted in compliance with MPEP 2001.06(b) and 2004. Paper copies of these applications as well as copies of applicants' copending applications cited in prior submitted Disclosure Statements are not required under MPEP 609.04(a) II and (c), wherein the requirement to submit copies to the PTO is waived. See the cited 1287 O.G. 163 (10/19/04).

The enclosed PTO 1449 lists related applications noted below. It is respectfully requested that the Examiner consider and make of record all of the cited applications listed in the enclosed PTO 1449 and in all prior submitted similar Disclosure Statements by initialing each item and returning a copy of the initialed paper to applicants.

Serial No.	Title	Inventors	Our Ref.
10/562,989	Method and Device for	Jurgen Ficker et al.	411000-143
	Patterning Organic Layers		
10/562,869	Logic Gate With A Potential-	Wolfram Glauert et al.	411000-144
	Free Gage Electrode for		
	Organic Integrated Circuits		111000 110
10/569,763	Organic Electronic Component	Walter Fix et al.	411000-146
	With High Resolution		
	Structuring And Method For		
	The Production Thereof		444000 447
10/568,730	Organic Capacitor With	Wolfgang Clemens et al.	411000-147
	Voltage-Controlled		
	Capacitance		444000 440
10/569,233	Polymer Mixtures for Printed	Adolf Bernds et al.	411000-148
	Electronic Circuits	\\\\\\\\\\\\\\\\\\\\\\\\\\\\\\\\\\\\\\	444000 440
10/570,571	Mechanical Control Elements	Wolfgang Clemens et al.	411000-149
	for Organic Polymer Electronic		
	Devices	NA har Fire at al	411000-150
10/585,775	Organic Transistor Comprising	Walter Fix et al.	411000-150
	a Self-aligning Gate Electrode		
	and Method for the Production		
14/574 400	Thereof Carable of	Lurgen Fielder et al	411000-153
11/574,139	External Package Capable of	Jurgen Ficker et al.	411000-100
1	Being Radio-Tagged		

The Commissioner is authorized to charge payment of any fees associated with this communication or credit any overpayment to Deposit Account No. 03-0678.

## FIRST CLASS CERTIFICATE

I hereby certify that this correspondence is being deposited on the date set forth below with the U.S. Postal Service as First Class Mail, postage prepaid, in an envelope addressed to:

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June 1, 2007

Janice Speidel #319396

Date

Respectfully submitted, Wolfgang Clemens et al.

William Equire

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Tel: (973)994-1700 Fax: (973)994-1744 PTO/SB/08a (08-03) Approved for use through 07/31/2006. OMB 0651-0031 U.S. Patent and Trademark Office; U.S. Department of Commerce persons are required to respond to a collection of information unless it displays a valid OMB control number

Substitute for form 1449A/PTO		Comp	Complete if Known	
			Application Number	10/542,678
INFORMATION DISCLOSURE STATEMENT BY APPLICANT		Filing Date	December 27, 2005	
		First Named Inventor	Adolf Bernds	
			Group Art Unit	2812
(Use as many sheets as necessary)		Examiner Name	Not assigned	
Sheet	1	1	Attorney Docket Number	411000-140

JUN n 4 2007

			U.S. PATENT DOC	UMENTS	
Examiner Initial*	Cite No. <sup>1</sup>	Document Number  Number-Kid Code <sup>2 (f known)</sup>	Publication- Date MM-DD-YYYY	Name of Patentee or Applicant of Cited Document	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear
					See Disclosure Statements filed herewith and previously
	143	US-10/562,989	06-29-2006	Jurgen Ficker et at.	
	144	US-10/52,869	10-05-2006	Walter Fix et al.	
	146	US-10/569,763	N/A	Walter Fix et al.	
	147	US-10/568,730	02-08-2007	Wolfgang Clemens et al.	
-	148	US-10/569,233	01-25-2007	Adolf Bernds et al.	
	149	US-10/570,571	01-11-2017	Wolfgang Clemens et al.	
	150	US-10/585,775	N/A	Walter Fix	
	153	US-11/574,139	N/A	Jurgen Ficker	
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Examiner S	ignature			Date Considered	

\*EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 609. Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant. Applicant's unique citation designation number (optional). See Kinds Codes of USPTO Patent Documents at www.uspto.gov or MPEP 901.04. DO NOT SEND FEES OR COMPLETED FORMS TO THIS ADDRESS. SEND TO: Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450.

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